METHODS AND APPARATUS FOR HANDLING RESET EVENTS IN A BUS BRIDGE

Abstract of the Disclosure

Methods and apparatus are provided for handling reset events in a bus bridge. A system on a programmable chip includes master components and slave components supporting various bus protocols. Bus bridges allow components using different bus protocols to interact. Reset of a distinct subset of programmable chip components or the synchronization of reset signals across disparate clock domains is allowed by effectively handling reset related signals at a bus bridge.